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(use as many sheets as necessary)

of 2 **Sheet**

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	Application Number	10/039,650	
	Filing Date	December 31, 2001	
	First Named Inventor	Shiv Kaushik	
	Art Unit	2183	
_	Examiner Name	Charles Harkness	
	Attorney Docket Number	42390P13636	

U.S. PATENT DOCUMENTS					
	Cite No.'	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pagas, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
Examiner Initials*		Number - Kind Code ² (if known)			
CH	AI	US-5,530,597	06-25-1996	Bowles et al.	
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Examiner Signature

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Parallelization, "Univ. of Wisc. Technical Report No. TR1430, October 2001, pgs. 1-21. Bradford et al., "Efficient Synchronization for Multithreaded Processors," Workshop on Multithreaded Execution, Architecture and Compilation (MTEAC), 01-31-1998, pgs. 1-4.	Examiner Initials*		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
Multithreaded Execution, Architecture and Compilation (MTEAC), 01-31-1998, pgs. 1-4.	14	BI	Parallelization, "Univ. of Wisc. Technical Report No. TR1430, October 2001, pgs. 1-21.					
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